

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
32079/2:7APPLICATION NO.
09/174,057INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)APPLICANT
Rubinstein, RichardFILING DATE
10/16/98GROUP
2785

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
HK	AA	5 8 4 1 4 4 4	11/24/98	Mun	345	506	
	AB	5 6 5 9 5 4 3	8/19/97	Ater	370	258	
	AC	5 4 4 8 7 1 5	9/5/95	Lelm	395	550	RECEIVED
	AD	5 3 9 6 6 3 4	3/7/95	Zaidi	395	800	JUL 28 1999
	AE	5 2 3 0 0 4 2	7/20/93	Masaki	395	162	Group 2700
	AF	4 8 6 2 4 0 7	8/29/89	Fette	364	900	
HK	AG	4 0 8 6 6 2 9	4/25/78	Desyllas	364	200	
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
HK	AL	DE19713178 A1	01/10/98	Germany	G06F	12/08		X
	AM	DE19735981 A1	26/3/98	Germany	G06F	9/38		X
	AN	WO 9734231	18/09/97	WIPO	G06F	12/12	X	
	AO	09034783	07/02/97	Japan	G06F	12/08	X	
HK	AP	EP 0741356 A1	06/11/96	EPO	G06F	12/08	X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

HK	AR	Article by Karl Gutttag et al, A SINGLE-CHIP MULTIPROCESSOR FOR MULTIMEDIA: THE MVP, 11/92
HK	AS	Conference materials by Yoshihiro Fujita et al, A DATAFLOW IMAGE PROCESSING SYSTEM TIP-4, 9/89
HK	AT	Article by Gordon Lang et al, AN OPTIMUM PARALLEL ARCHITECTURE FOR HIGH-SPEED REAL-TIME DIGITAL SIGNAL PROCESSING, 2/88

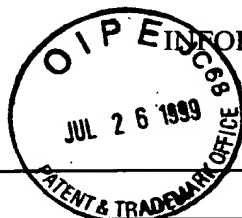
EXAMINER

Harold K.

DATE CONSIDERED

6/17/2000

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
HK	AL	DE 4432217 A1	16/03/95	Germany	G11C	7/00		X
	AM	WO 9412929	09/06/94	WIPO	G06F	9/26	X	
	AN	EP 0590807 A2	06/04/94	EPO	G06F	15/44	X	
	AO	EP 0498107 A2	12/08/92	EPO	G11C	5/14	X	
	AP	2155671 A	25/9/85	U.K.	G06F	13/00	X	
HK	AR	EP 0139254 A2	02/05/85	EPO	G06F	13/28	X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

HK	AS	Paper by M.C. Ertem, A RECONFIGURABLE CO-PROCESSOR FOR MICROPROCESSOR SYSTEMS, 6/87
HK	AT	Article by A.C. Davies et al, INTERFACING A HARDWARE MULTIPLIER TO A GENERAL PURPOSE MICROPROCESSOR, 10/77

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Harold Kim

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